**PRODUCT:** TFT TOUCH MODULE

**MODULE NO.:** WTY070144A11-K01

**SUPPLIER:** Wanty Photoelectric

Co.,LtdDATE: 2021.06.03

### **SPECIFICATION**

Revision: 0.0

#### WTY070144A11-K01

This module uses ROHS material

This specification may change without prior notice in order to improve performance or quality. Please contact Wanty department for updated specification and product status before design for this product or release of this order.

### **REVISION RECORD**

REV NO.	REV DATE	CONTENTS	REMARKS
0.0	2020.05.15	First release	Preliminary

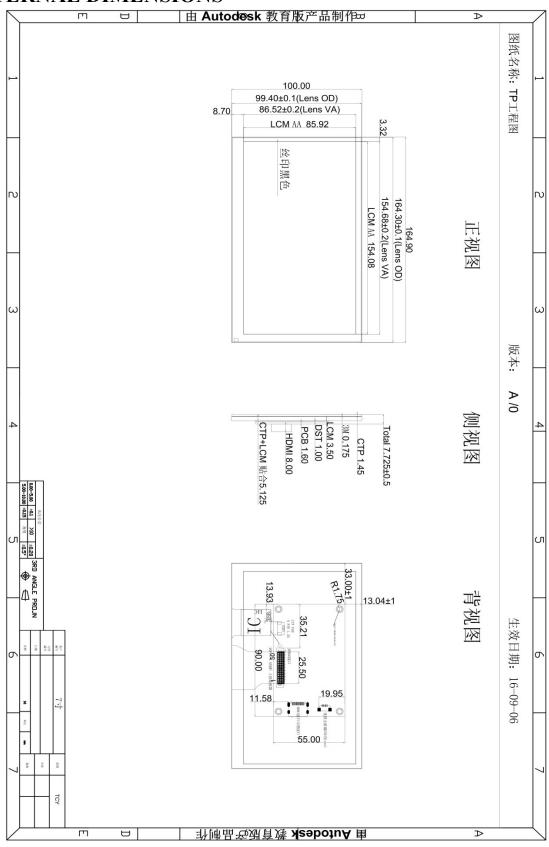
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### 1, GENERAL INFORMATION

Item of general information		Contents		
LCD Display Size(Diagonal)		7		
Module Structure	CTP Touc	CTP Touch + LCD Display + PCB		
LCD Display Type	TFT/	TRANSMISSIVE	-	
LCD Display Mode	N	ormally White	-	
Viewing Direction		All (IPS)	o'clock	
Module size (W×H)	1	00×164.9.00	mm	
Active area (W×H)	8:	5.92X154.21	mm	
Number of pixels(Resolution)	1	pixel		
Pixel pitch (W×H)	0.1506×0.1432		mm	
Color Pixel Arrangement		RGB Stripe	-	
M - 1-1- 1-4	LCD Display	HDMI interface	-	
Module Interface Type	CTP Touch	USB interface	-	
Creations Commont	Win7/Win	-		
System Support	Android/Linux	-		
Power Supply	USB(5.0V)		-	
Module Power consumption	450-750		mA	
Color Numbers	16.7M		-	
Backlight Type		White LED	-	

#### 2, EXTERNAL DIMENSIONS



#### 3, ABSOLUTE MAXIMUM RATINGS

Parameter of absolute maximum ratings	Symbol	Min	Max	Unit
Operating temperature	Top	-10	50	${\mathbb C}$
Storage temperature	Tst	-20	60	$^{\circ}$
Humidity	RH	-	90%(Max 60°C)	RH

Note: Absolute maximum ratings means the product can withstand short-term, not more than 120 hours. If the product is a long time to withstand these conditions, the life time would be shorter.

### 4、ELECTRICAL CHARACTERISTICS(DC CHARACTERISTICS)

Parameter of DC characteristics	Symbol	Min.	Тур.	Max.	Unit
PCB operating voltage	VUSB	-	5.0	-	V
LCD I/O operating voltage	VDD	3.0	3.3	3.6	V
Input voltage 'H' level	VIH	0.7*VDD	-	VDD	V
Input voltage 'L' level	VIL	VSS	-	0.3*VDD	V
Output voltage 'H' level	VOH	VDD-0.4	-	VDD	V
Output voltage 'L' level	VOL	VSS	-	VSS+0.4	V

### **5. CTP CHARACTERISTICS**

Item of CTP	Specification	Unit	Remark
Panel Type	Glass Cover + Glass Sensor	-	-
Resolution	1024×600	pixel	-
Surface Hardness	≥6H	-	-
Transparency	>82%	-	-
Driver IC	-	-	-
Interface Type	USB	-	-
Support Points	5	-	-
Sampling Rate	20~100	Hz	-
Supply voltage	3.3	V	-

#### 6 ELECTRO-OPTICAL CHARACTERISTICS

Item of electro-op	otical	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark	Note
Response	time	Tr+Tf	0-0	-	25	40	ms	FIG 1.	4
Contrast F	Ratio	CR	θ=0 ∅=0	-	320	-	-	FIG 2.	1
Luminance un	iformity	δWHITE	Ta=25℃	-	80	-	%	FIG 2.	3
Surface Lum	ninance	Lv		180	220	-	cd/m2	FIG 2.	2
	White	White x		-	0.302	-			
	Willie	White y		-	0.338	-		FIG 2.	
	Red	Red x		-	0.606	-			
CIE (x, y)	Reu	Red y	θ=0 ∅=0	-	0.325	-			5
chromaticity	Green	Green x	7a=25°C	-	0.303	_	-	rio 2.	3
	Green	Green y	14 25 0	-	0.567	-			
	Blue	Blue x		-	0.147	-			
	Blue	Blue y		-	0.161	-			
	Ø=90(1	2 o'clock)		-	85	-	deg		
Viewing	Ø=270(	6 o'clock)	CD > 10	-	85	-	deg	FIG 3.	6
angle range	Ø=0(3 d	o'clock)	CR ≥ 10	-	85	-	deg	FIG 3.	0
	Ø=180(	9 o'clock)		-	85	-	deg		
NTSC ratio		-	-	-	50	-	%	-	-

**Note 1.** Contrast Ratio(CR) is defined mathematically by the following formula. For more information see FIG 2.:

 $Contrast\ Ratio(CR) = \frac{Average\ Surface\ Luminance\ with\ all\ white\ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}{Average\ Surface\ Luminance\ with\ all\ black\ pixels(P1,P2,P3,P4,P5,P6,P7,P8,P9)}$ 

**Note 2.** Surface luminance is the LCD surface from the surface with all pixels displaying white. For more information see FIG 2.

Lv=Average Surface Luminance with all white pixels (P1,P2,P 3,P4, P5,P6,P7,P8,P9)

Note 3. The uniformity in surface luminance ( $\delta$ WHITE) is determined by measuring

luminance at each test position 1 through 9, and then dividing the maximum luminance of 9 points luminance by minimum luminance of 9 points luminance. For more information see FIG 2.

$$\delta \text{WHITE} = \frac{\textit{Minimum Surface Luminance with all white pixels} (P1, P2, P3, P4, P5, P6, P7, P8, P9)}{\textit{Maximum Surface Luminance with all white pixels} (P1, P2, P3, P4, P5, P6, P7, P8, P9)}$$

**Note 4.** Response time is the time required for the display to transition from White to black(Rise Time, Tr) and from black to white(Decay Time, Tf). For additional information see FIG 1.

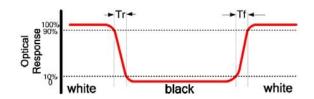
**Note 5.** CIE (x, y) chromaticity ,The x,y value is determined by screen active area position 5. For more information see FIG 2.

**Note 6.** Viewing angle is the angle at which the contrast ratio is greater than a specific value. For TFT module, the specific value of contrast ratio is 10. The angles are determined for the horizontal or x axis and the vertical or y axis with respect to the z axis which is normal to the LCD surface. For more information see FIG 3.

**Note 7.** For Viewing angle and response time testing, the testing data is base on Autronic-Melchers's ConoScope. Series Instruments. For contrast ratio, Surface Luminance, Luminance uniformity and CIE, the testing data is base on BM-7 photo detector.

**Note 8.** For TN type TFT transmissive module, Gray scale reverse occurs in the direction of panel viewing angle.

#### FIG.1. The definition of Response Time



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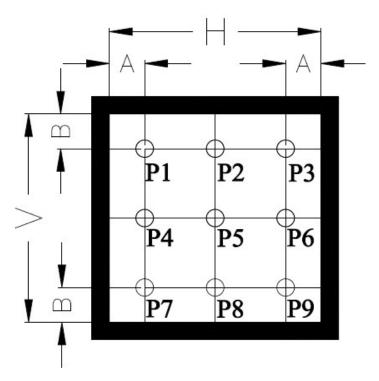
#### FIG.2. Measuring method for Contrast ratio, surface luminance, Luminance

#### uniformity, CIE (x, y) chromaticity

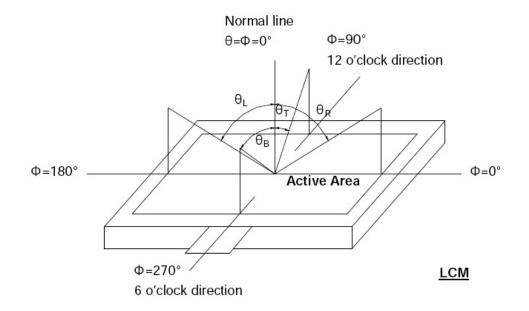
A: H/6; B: V/6;

H,V: Active Area(AA) size

Measurement instrument: BM-7; Light spot size=5mm, 350mm distance from the LCD surface to detector lens.



#### FIG.3. The definition of viewing angle



#### 7, INTERFACE DESCRIPTION

#### A, **HDMI** Interface Description

NO.	Symbol	DESCRIPTION
1	TMDS Data2+	Positive side of channel 2 TMDS low-voltage signal differential input pair
2	TMDS Data2 Shield	Ground
3	TMDS Data2-	Negative side of channel 2 TMDS low-voltage signal differential input pair
4	TMDS Data1+	Positive side of channel 1 TMDS low-voltage signal differential input pair
5	TMDS Data1 Shield	Ground
6	TMDS Data1-	Negative side of channel 1 TMDS low-voltage signal differential input pair
7	TMDS Data0+	Positive side of channel 0 TMDS low-voltage signal differential input pair
8	TMDS Data0 Shield	Ground
9	TMDS Data0-	Negative side of channel 0 TMDS low-voltage signal differential input pair
10	TMDS Clock+	Positive side of reference clock. TMDS low-voltage signal differential input pair
11	TMDS Clock Shield	Ground
12	TMDS Clock-	Negative side of reference clock. TMDS low-voltage signal differential input pair
13	CEC	No Connection
14	Reserved(N.C.)	No Connection
15	SCL	DDC SCL
16	SDA	DDC SDA
17	DDC/CEC Ground	Ground
18	+5V Power	+5V Power
19	Hot Plug Detect	Hot Plug Detect

### **B**、 USB Interface Description

Symbol	DESCRIPTION
VUSB	USB Power
D-	USB Data-
D+	USB Data+
NC	No connection
GND	Power Ground

Application Note: Please connect the USB first, and then connect the HDMI interface.
8、LCD TIMING

### **Horizontal input Timing table**

Parameter	Symbol		Value		Unit
r at ameter	Symbol	Min.	Тур.	Max.	Unit
DCLK frequency@ Frame rate=60Hz	DCLK	44.9	51.2	63	MHz
Horizontal display area	thd	1024			DCLK
1 Horizontal Line	th	1200	1344	1400	DCLK
HSYNC pulse width	thpw	1	-	140	DCLK
HSYNC Blanking	thb	160	160	160	DCLK
HSYNC Front Porch	thfp	16 160 216		DCLK	
Vertical display area	tvd	600			Н
VSYNC period time	tv	624	635	750	Н
VSYNC pulse width	tvpw	1	-	20	Н
VSYNC Blanking	tvb	23	23	23	Н
VSYNC Front Porch	tvfp	1	12	127	Н

#### Vertical input Timing table

Parameter	Symbol		Value		Unit
1 at ameter	Symbol	Min.	Тур.	Max.	
DCLK frequency@ Frame rate=60Hz	DCLK	40.8	51.2	67.2	MHz
Horizontal display area	thd	1024		DCLK	
HSYNC period time	th	1114 1344 1400		DCLK	
HSYNC Blanking	HSYNC Blanking thb + thfp 90 320 376		376	DCLK	
Vertical display area	tvd	600		Н	
VSYNC period time	tv	610	635	800	Н
VSYNC Blanking	tvb + tvfp	10	35	200	Н

#### 9、RELIABILITY TEST CONDITIONS

No.	Test Item	Test Condition
1	High Temperature Storage	60°C/120 hours
2	Low Temperature Storage	-20°C/120 hours
3	High Temperature Operating	50°C/120 hours
4	Low Temperature Operating	-10°C/120 hours
5	Temperature Cycle Storage	-10°C(30min.)~25(5min.)~60°C(30min.)×10cycles

#### A. Inspection after test:

Inspection after 2~4 hours storage at room temperature, the sample shall be free from defects:

- Air bubble in the LCD;
- > Sealleak;
- ➤ Non-display;
- Missing segments;
- ➤ Glass crack;
- Current is twice higher than initial value.

#### B. Remark:

- ➤ The test samples should be applied to only one test item.
- Sample size for each test item is  $5\sim10$  pcs.
- Failure Judgment Criterion: Basic Specification, Electrical Characteristic, Mechanical Characteristic, Optical Characteristic.

#### 10 INSPECTION CRITERION

This specification is made to be used as the standard of acceptance/rejection criteria for TFT-LCD/IPS TFT-LCD module product, and this specification is applicable only in the case that the size of module equal to or exceed than 3.5 inch.

#### 10.1 Sample plan

Sampling plan according to GB/T2828.1-2003/ISO 2859-1: 1999 and ANSI/ASQC Z1.4-1993,normal level 2 and based on:

Major defect: AQL 0.65

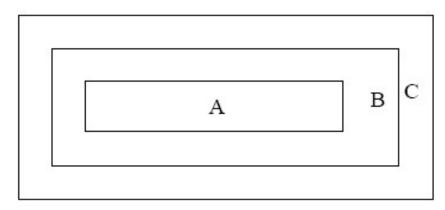
Minor defect: AQL 1.5

#### **10.2** Inspection condition

Viewing distance for cosmetic inspection is about 30cm with bare eyes, and under an environment of  $20\sim40W$  light intensity, all directions for inspecting the sample should be within  $45^{\circ}$  against perpendicular line. (Normal temperature  $20\sim25^{\circ}$ C and normal humidity 60  $\pm15\%$ RH)

#### 10.3 Definition of Inspection Item.

#### A. Definition of inspection zone in LCD.



Zone A: character/Digit area

Zone B: viewing area except Zone A (Zone A + Zone B=minimum Viewing area)

Zone C: Outside viewing area (invisible area after assembly in customer's product)

Fig.1 Inspection zones in an LCD

Note: As a general rule, visual defects in Zone C are permissible, when it is no trouble for quality and assembly of customer's product.

#### B \ Definition of some visual defect

	Because of losing all or part function, bad pixel dots appear bright and the
Bright dot	size is more than 50% of one dot in which LCD panel is displaying under
	black pattern.
Doub dat	Dots appear dark and unchanged in size in which LCD panel is displaying
Dark dot	under pure red, green, blue picture, or pure whiter picture.

#### 10.4 Major Defect

Item No.	Items to be inspected	Inspection standard	Classification of defects
1	Functional defects	1) No display 2) Display abnormally 3) Missing vertical, horizontal segment 4) Short circuit 5) Excess power consumption 6)Backlight no lighting, flickering and abnormal lighting	major
2	Missing	Missing component	
3	Outline dimension	Overall outline dimension beyond the drawing is not allowed	

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#### 10.5 Minor Defect

Item	Items to be	Inspection standard						Classification
No.	inspected	_	of defects					
		Zone			Accepta A+B	able Qty		
				3.5"~7		, >10.1"		
		Bright pixel do	ot	1	2	3		
	Dui alet dat	Dark pixel dot		4	4	4		
1	Bright dot /dark dot	2bright dots adjacent		0	0	0	Acceptable	Minor
1	defect	2dark dots adja	cent	0	0	0	ptab	Millor
	derect	Total bright and dots	dark	5	6	7	le	
		Note: Minimum dist Pixel dots' function material and other re						
		Zone			Acceptable			
				A+B				
		Size(mm)	3.5"~	5"~7" 7~10.1" >10.1"		>10.1"	С	
		Φ≤0.2	Accept	table A	Acceptable	Acceptable	Acc	
2		0.2< Ф ≤ 0.5	4	5		6	Acceptable	Minor
		Ф>0.5	0		0	0	ble	
		Note: 1. Minimum distance 2. The quantity of december 1.				nm;		
		Zone Acceptable Qty						
3	Linear defect	Size (mm)	A+B					
		Length Width	3.5"~	-7"	7~10.1"	>10.1"	С	Minor
		Ignore W≤0.05	Accept	table	Acceptable	Acceptable	Ac	1,11101
			$ \begin{array}{ c c c } \hline L \leqslant 5.0 & 0.05 < \\ \hline W \le 0.1 \end{array} $	4		5	6	Acceptable
		L>5.0 W>0.1	0		0	0	ē	

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		5.4.1 Pola	rizer Posit	ion				
		(i) Shiftir						
		dimension			-			
		(ii) Incor	mplete cov	ering of the vi	ewing area du	e to shifting i	s not	
		allowed.	-	=	-	J		
		5.4.2 Dirt	on polariz	er				
		Dirt which						
		5.4.3 Pola						
			Zone					
				A+B				
		Size(mm	1)	3.5"~7" 7~10.1" >10.1		>10.1"	С	
		Ф <	€0.2	Acceptable	Acceptable	Acceptable	Ac	
		0.2<	Ф≤0.5	4	5	6	Acceptable	
4	Polarizer	Φ>	>0.5	0	0	0	ble	Minor
	defect	5.4.4 Pol	arizer scra	atch	<u> </u>	I		
		(i) If the 1						
		or in the						
		( ii )If the						
		condition						
		Zone		Acceptable Qty				
		Size (mm)		A+B				
		Length	Width	3.5"∼7"	7~10.1"	>10.1"	С	
		Ignore	W≤0.05	Acceptable	Acceptable	Acceptable	Ac	
		1.0 <l< td=""><td>0.05&lt;</td><td></td><td></td><td></td><td rowspan="2">Acceptable</td></l<>	0.05<				Acceptable	
		≤5.0	W≤0.20	4	5	6		
		L>5.0	W>0.2	0	0	0	е	
5	MURA	Using 3	N.C.					
	White/Black dot (MURA)	Vi 0.15	Minor					

		(i) Crack				
		Cracks are	not allowed.	//		
			Minor			
		(ii) TFT chips	s on corner			
		(ii) If I chips	z	+		Minor
6	Glass	X	Y	Z	Acceptable	
	defect	≤3.0	≤3.0	Not more than the	N≤3	
		Chins on the	corner of term	thickness of glass inal shall not be allowed	d to extend	
				perimeter seal.	i to catche	
		(iii) Usual sur				
		Z				Minor
		X	Y	Z	Acceptable	
		≤1.5	≤1.5	Not more than the thickness of glass	N≤4	
		It i	s only applica	ble to the upper glass of	FLCD.	

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#### 10.6 Module Cosmetic Criteria

Item No.	Items to be inspected	Classification of defects	
1	Difference in Spec.	Not allowable	Major
2	Pattern peeling	No substrate pattern peeling and floating	Major
		No soldering missing	Major
3	Soldering defects	No soldering bridge	Major
		No cold soldering	Minor
_	D. i. d. D.C.D.	Visible copper foil (Φ0.5 mm or more) on substrate	3.6
4	Resist flaw on PCB	pattern is not allowed	Minor
5	FPC gold finger	No dirt, breaking, oxidation lead to black	Major
6	Backlight plastic frame	No deformation, crack, breaking, backlight positioning column breaking, obvious nick.	Minor
7	Marking printing effect	No dark marking, incomplete, deformation lead to unable to judge	Minor
8	Accretion of metallic Foreign matter	No accretion of metallic foreign matter (Not exceed Φ0.2mm)	Minor
9	Stain	No stain to spoil cosmetic badly	Minor
10	Plate discoloring	No plate fading, rusting and discoloring	Minor
	1. Lead parts	a. Soldering side of PCB Solder to form a 'Filet' all around the lead. Solder should not hide the lead form perfectly.	Minor
		b. Components side(In case of 'Through Hole PCB') Solder to reach the Components side of PCB.	Minor
	2. Flat packages	Either 'Toe'(A) or 'Seal'(B)of the lead to be covered by "Filet". Lead form to be assume over Solder.	Minor
11	(3/2) H ≥h ≥(1/2) H  3. Chips		Minor
	4. Solder ball/Solder splash	a. The spacing between solder ball and the conductor or solder pad h $\geq$ 0.13 mm. The diameter of solder ball d $\leq$ 0.15 mm.	Minor
		b. The quantity of solder balls or solder splashes isn't beyond 5 in 600 mm2.	Minor
		c. Solder balls/Solder splashes do not violate minimum electrical clearance.	Major